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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/796,157	03/10/2004	Tomoharu Tanaka	249781US-2S CONT	4507
22850	7590 06/25/2004		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET			HOANG, HUAN	
ALEXANDRIA, VA 22314			ART UNIT	PAPER NUMBER
			2818	
			DATE MAILED: 06/25/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

		- A/	
	Application No.	Applicant(s)	
	10/796,157	TANAKA, TOMOHARU	
Office Action Summary	Examiner	Art Unit	
	Huan Hoang	2818	
The MAILING DATE of this communication app Period for Reply	ars on the cover sheet with the	correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on	_•		
2a) ☐ This action is FINAL . 2b) ☑ This	action is non-final.		
3) Since this application is in condition for allowar closed in accordance with the practice under E			
Disposition of Claims			
 4) Claim(s) 31-45 is/are pending in the application 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 31-45 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or 	vn from consideration.	,-	
Application Papers			
9) The specification is objected to by the Examine	г.		
10) The drawing(s) filed on is/are: a) □ acce	epted or b) \square objected to by the I	Examiner.	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Ex			
Priority under 35 U.S.C. § 119			
12) △ Acknowledgment is made of a claim for foreign a) △ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documents 2. △ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive ı (PCT Rule 17.2(a)).	on No. <u>10/373,920</u> . ed in this National Stage	
Attachment(s)	_		
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) ☐ Interview Summary Paper No(s)/Mail Da		
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 031004. 		ratent Application (PTO-152)	

DETAILED ACTION

The Preliminary Amendment filed on 03/10/04 has been received and entered.

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Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 31-45 are rejected under 35 U.S.C. 102(b) as being anticipated by Hosono et al. (EP1 134 746 A2 cited by Applicant).

Hosono et al. (Fig. 2 and Fig. 13) show a nonvolatile semiconductor device having all the elements as recited in claims 31-45 as follows:

- at least one memory cell (500, Fig. 2);
- a bit line (Ble, Fig. 2) connected to the memory cell;
- a bit line control circuit including a capacitor (48, Fig. 2), a MOS transistor (44, Fig. 2), a flip-flop circuit (1a, Fig. 2), a first transfer gate (41, Fig. 2), a second transfer gate (42, Fig. 2), and a third transfer gate (45, Fig. 2), wherein a source electrode of the MOS transistor is coupled to the capacitor (48), a drain electrode of the MOS transistor is coupled to a voltage source (Vdd), and the MOS transistor selectively changes an amount of charges stored in the capacitor according to a voltage level of the gate electrode selectively boosted by a change of the voltage source (VREG, Vdd+α, Fig. 13);

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- a fourth transfer voltage (43, Fig. 3); and

- the bit line during program of the memory cell having one of three potential levels controlled by the flip-flop circuit and the voltage level of the gate electrode of the MOS transistor (column 18, 7-9 and paragraph [0115]).

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Shibata et al. discloses a semiconductor memory device.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Huan Hoang whose telephone number is (571) 272-1779. The examiner can normally be reached on Mon-Fri 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Huan Hoang Primary Examiner Art Unit 2818 Page 4

HH 6/24/04.